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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/707,608	12/24/2003	CHIH-FENG SUNG	10217-US-PA	1607	
31561	7590 01/14/2005		EXAM	EXAMINER	
•	JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE			TRAN, THUY V	
7 FLOOR-1, ROOSEVEL	, NO. 100 T ROAD, SECTION 2		ART UNIT	PAPER NUMBER	
	00	,	2821		
TAIWAN		,	DATE MAILED: 01/14/2005	DATE MAILED: 01/14/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
Office Action Summany	10/707,608	SUNG, CHIH-FENG	
Office Action Summary	Examiner	Art Unit	
	Thuy V. Tran	2821	
The MAILING DATE of this communication appeared for Reply	ppears on the cover sheet with th	e correspondence address	•
A SHORTENED STATUTORY PERIOD FOR REP. THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory perior - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).		e timely filed days will be considered timely. from the mailing date of this communicat DNED (35 U.S.C. § 133).	tion.
Status			
1) Responsive to communication(s) filed on 24	December 2003.		
	is action is non-final.		
3) Since this application is in condition for allow closed in accordance with the practice under	·	•	is
Disposition of Claims			
4) Claim(s) 1-11 is/are pending in the applicatio 4a) Of the above claim(s) is/are withdres 5) Claim(s) is/are allowed. 6) Claim(s) 1-11 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/ Application Papers 9) The specification is objected to by the Examination of the drawing(s) filed on 24 December 2003 is/ Applicant may not request that any objection to the	awn from consideration. /or election requirement. ner. /are: a)⊠ accepted or b)□ obje	•	
Replacement drawing sheet(s) including the corre		-	• •
Priority under 35 U.S.C. § 119			
a) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of: 1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	nts have been received. nts have been received in Applic ority documents have been rece au (PCT Rule 17.2(a)).	cation No eived in this National Stage	
Attachment(s) X Notice of References Cited (PTO-892) X Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summ Paper No(s)/Mai		
Notice of Dialisperson's Patent Drawing Review (P10-946) Information Disclosure Statement(s) (PT0-1449 or PT0/SB/08 Paper No(s)/Mail Date 12/24/03; 8/2/04.		al Patent Application (PTO-152)	

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6)

DETAILED ACTION

This is a response to the Applicant's filing on 12/24/2003. In virtue of this filing, claims 1-11 are currently presented in the instant application.

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statements

2. The information disclosure statements (IDSs) submitted on 12/24/2003 and 08/02/2004 are in compliance with the provisions of 37 CFR 1.97. Accordingly, these information disclosure statements are considered by the examiner.

Drawings

3. The drawings submitted on 12/24/2003 are accepted.

Specification Objections

4. The specification of the disclosure is objected to because of the following informalities:

Paragraph [0026], line 12, "Ito" should be changed to --I to--;

Paragraph [0029], line 15, "P9408" should be changed to --P(408)--; and

Paragraph [0031], line 1, "conclusions" should be changed to --conclusion--.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claims 1-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Troutman (U.S. Patent No. 6,157,356).

With respect to claim 1, Troutman discloses, in Figs. 1A and 1B, an organic light-emitting display (see col. 1, line 62) having a plurality of pixels (se Fig. 1B) and at least one external power line (into V_b; see Fig. 1A and col. 2, line 29) and being characterized in that the external power line diverts into a plurality of internal power lines to connect to the pixels (see Fig. 1B), wherein each of the internal power lines is segmented into at least two separated parts.

With respect to claim 2, Troutman discloses, in Figs. 1A and 1B, that the external power line is coupled to a power source $[V_b]$.

With respect to claim 3, Troutman discloses, in Figs. 1A and 1B, that the power source [V_b] supplies an electric current, and the electric current flows through the internal power lines to reach the pixels.

With respect to claim 4, Troutman discloses, in Figs. 1A and 1B, that the pixels are arranged in a pixel array (see Fig. 1B).

With respect to claim 5, Troutman discloses, in Figs. 1A and 1B, that each of the pixels comprises (i) a switching transistor [102] having a first drain electrode, a first gate electrode, and a first source electrode, wherein the first drain electrode is coupled to a data line [112], and the first gate electrode is coupled to a scan line [110], (ii) a driving transistor [106] having a second drain electrode, a second gate electrode, and a second source electrode, wherein the second source electrode is grounded, (iii) a storage capacitor [104] having a first terminal and a second terminal, wherein the first terminal is coupled to the first source electrode and the second gate

electrode, and the second terminal is grounded and coupled to the second source electrode, and (iv) a light-emitting device [108] having an anode and a cathode, wherein the anode is coupled to one of the internal power lines (from V_b; see Fig. 1A) and the cathode is coupled to the second drain electrode.

With respect to claim 6, Troutman discloses, in Figs. 1A and 1B, that one terminal of each of the internal power lines is coupled via the external power line to a positive power source.

With respect to claim 7, Troutman discloses, in Figs. 1A and 1B, that the switching transistor [102] comprises a thin film transistor (see col. 2, lines 25-28).

With respect to claim 8, Troutman discloses, in Figs. 1A and 1B, that the driving transistor [106] comprises a thin film transistor (see col. 2, lines 25-28).

With respect to claim 9, Troutman discloses, in Figs. 1A and 1B, that the light-emitting device comprises an organic light-emitting diode [108].

With respect to claim 10, Troutman discloses, in Figs. 1A and 1B, that the light-emitting device comprises a polymer light-emitting diode (see col. 2, lines 23-24).

With respect to claim 11, Troutman discloses, in Figs. 1A and 1B, that the organic light-emitting device comprises an active matrix organic light emitting display (see Fig. B; col. 1, line 62).

Citation of relevant prior art

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Prior art Shannon (U.S. Patent No. 6,806,654) discloses a matrix display.

Prior art Shieh et al. (U.S. Patent No. 5,748,160) discloses active driven LED matrices.

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Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -5:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-21/7-9197 (toll-free).

Thuy V. Trable Primary Examiner Art Unit 2821

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